

Case Docket No. NETL.001DV4

Date: March 29, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Jayesh R. Bhakta et al.

Appl. No.

10/765,420

Filed

January 27, 2004

For

ARRANGEMENT OF INTEGRATED

CIRCUITS IN A MEMORY MODULE

Examiner

Unknown

Group Art Unit:

Unknown

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

March 29, 2004

(Date)

Bruce S. Itchkawitz, Reg. No. 47,67

TRANSMITTAL LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement.
- (X) A PTO Form 1449 listing fifty-nine (59) references.
- (X) A return prepaid postcard.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.

Bruce S. Itchkawitz

Registration No. 47,677 Attorney of Record

Customer No. 20,995

(949) 760-0404



INFORMATION DISCLOSURE STATEMENT

Applicants

Jayesh R. Bhakta et al.

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CIRCUITS IN A MEMORY MODULE

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing fifty-nine (59) references. Copies of disclosed U.S. patents and/or publications are not included pursuant to the PTO waiver of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for applications filed after June 30, 2003. References listed under "Foreign Patent Documents" and "Other Documents" are of record in U.S. patent application No. 10/094,512, filed March 7, 2002, which is the parent of this divisional application, and is relied upon for an earlier filing date under 35 U.S.C. § 120. Copies of these references are not submitted pursuant to 37 C.F.R. § 1.98(d).

This Information Disclosure Statement is being filed before the receipt of a first Office Action on the merits, and presumably no fee is required in accordance with 37 C.F.R. § 1.97(b)(3). If a first Office Action on the merits was mailed before the mailing date of this Statement, the Commissioner is authorized to charge the fee set forth in 37 C.F.R. § 1.17(p) to Deposit Account No. 11-1410.

Appl. No.

: 10/765,420

Filed

January 27, 2004

Docket No. NETL.001DV4

Customer No. 20,995

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: $\frac{3/29/04}{}$

y: <u>/ X</u>

Bruce S. Itchkawitz

Registration No. 47,677

Attorney of Record

Customer No. 20,995

(949) 760-0404

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	FORM PTO-1449	U.S. DEPARTMENT OF C PATENT AND TRADEM
$\left(\right.$		N DISCLOSURE STATEMENT BY APPLICANT
/	<i>&/</i>	AL SHEETS IF NECESSARY)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY.	DOCKET	NO
NET	L.001DV4	

APPLICATION NO. 10/765,420

APPLICANTS
Jayesh R, Bhakta et al.

FILING DATE January 27, 2004 **GROUP** Unknown

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	4,280,176	07/21/81	Tan			
	2	4,679,167	07/07/87	Finnell			
	3	5,012,389	04/30/91	Perry			
	4	5,164,916	11/17/92	Wu et al.			
	5	5,191,404	03/02/93	Wu et al.			
,	6	5,226,136	07/06/93	Nakagawa			
	7	5,303,123	04/12/94	Chandler et al.			
	8	5,383,148	01/17/95	Testa et al.			
	9	5,412,538	05/02/95	Kikinis et al.			
	10	5,428,762	06/27/95	Curran et al.			
	11	5,465,229	11/07/95	Bechtolsheim et al.			
	12	5,495,435	02/27/96	Sugahara			
	13	5,513,135	04/30/96	Dell et al.			
	14	5,532,954	07/02/96	Bechtolsheim et al.			
	15	5,541,448	07/30/96	Carpenter			,
_	16	5,572,691	11/05/96	Koudmani			
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	18	5,642,323	06/24/97	Kotani et al.			
	19	5,652,462	07/29/97	Matsunaga et al.			
	20	5,652,861	07/29/97	Mayo et al.			
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	22	5,691,946	11/25/97	DeBrosse et al.			
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	24	5,737,192	04/07/98	Linderman			
	25	5,754,408	05/19/98	Derouiche			
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DATE CONSIDERED

FORM	PTO-1449
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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE ATTY. DOCKET NO. NETL.001DV4

APPLICATION NO. 10/765,420

INFORMATION DISCLOSURE STATEMENT

(USE SEVERAL SHEETS IF NECESSARY)

BY APPLICANT

APPLICANTS Jayesh R. Bhakta et al.

FILING DATE January 27, 2004 **GROUP** Unknown

				U.S. PATENT DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	29	5,953,738	09/14/99	Rao			
	30	5,973,951	10/26/99	Bechtolsheim et al.			
	31	6,021,048	02/01/00	Smith			
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	33	6,097,619	08/01/00	Nguyen et al.			
	34	6,151,235	11/21/00	Kitagawa et al.			
	35	6,157,538	12/05/00	Ali et al.			
	36	6,181,004 B1	01/30/01	Koontz et al.			
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	38	6,222,737 B1	04/24/01	Ross			,
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	40	6,353,539 B1	03/05/02	Horine et al.			
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	46	2003/0048616 A1	03/13/03	Ko et al.			,
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. , ,, .	49	2003/0061447 A1	03/27/03	Perego et al.			
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FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. NETL.001DV4	APPLICATION NO. 10/765,420	
	DISCLOSURE STATEMENT Y APPLICANT	APPLICANTS Jayesh R. Bhakta et al.		
(USE SEVERAL	SHEETS IF NECESSARY)	FILING DATE January 27, 2004	GROUP Unknown	

FOREIGN PATENT DOCUMENTS								
EXAMINER		DOCUMENT NUMBER	DATE	COUNTRY	CLASS SUBCLASS		TRANSLATION	
INITIAL						·	YES	NO
	51	JP 60034054 A	02/21/85	Japan				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)					
		PCT International Search Report dated August 29, 2003 for counterpart PCT application No. PCT US/03/06978 filed March 6, 2003, in 5 pages.				
		JEDEC Standard No. 21-C, 4.20.2 - 168 Pin, PC133 SDRAM Registered DIMM Design Specification, Revision 1.4, Release 11a, February 2002.				
	 JEDEC Standard No. 21-C, 4.20.3 - 144 Pin, PC133 SDRAM Unbuffered SO-DIMM, Reference Design Specifical Revision 1.02, Release 11. JEDEC Standard No. 21-C, DDR SDRAM PC2100 and PC1600 DDR SDRAM Registered DIMM Design Specifical Revision 1.3, Release 11b, January 2002. 					
		JEDEC Standard No. 21-C, 4.20.5 - 184 Pin PC1600/2100 DDR SDRAM Unbuffered DIMM Design Specification, Revision 1.1, Release 11b.				
	57 JEDEC Standard No. 21-C, 4.20.6 - 200 Pin, PC2700/PC2100/PC1600 DDR SDRAM Unbuffered SO- Design Specification, Revision 1.1, Release 11b, April 26, 2002.					
	58 Intel, PC SDRAM Registered DIMM Design Support Document, Revision 1.2, October 1998.					
		Intel Corporation, 66/100 MHz PC SDRAM 64-Bit Non-ECC/Parity 144 Pin Unbuffered SO-DIMM Specification, Revision 1.0, February 1999.				

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EXAMINER

DATE CONSIDERED